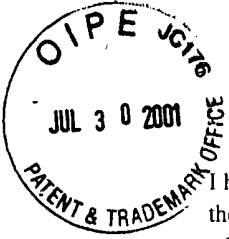


#3 Pre Amenda

Sur Docket No.: 44176.00033

Page 1 of 11

P. Br. 2
9/10/01



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF MAILING

I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to the Honorable Commissioner of Patents, Washington, D.C. 20231, on

Date: 7-25-01

By: Patrick D. Benedicto
Patrick D. Benedicto

In re Application of:

Ejaz Ul Haq

Serial No. 09/851,622

Examiner: Unknown

Filing Date: May 8, 2001

Art Unit: 2816

Title: High Speed Source Synchronous Signaling For Interfacing VLSI CMOS Circuits To Transmission Lines

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TECHNOLOGY CENTER 2816

Honorable Commissioner of Patents
Washington, D.C. 20231

PRELIMINARY AMENDMENT

Sir:

Please enter the following amendments in the above-identified application prior to examination.

In The Claims: /

Please amend claims 1, 4, 9, 14, 19-20, 22-23, 25, and 27 as follows:

1. A method of detecting a transition in an incoming signal from a known previous logical state, comprising:

obtaining an oscillating reference;

receiving an incoming signal; and

comparing the oscillating reference against the incoming signal using a differential amplifier to detect a transition in the incoming signal relative to the known previous logical state.

Sub
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